

unpatentable over JP '412 in view of U.S. Patent No. 4,448,492 to Huffman ("Huffman").

Claim 32 is rejected under 35 U.S.C. § 103(a) as being unpatentable over JP '412 in view of U.S. Patent No. 5,249,070 to Takano ("Takano").

The rejection of claims 1, 3-5, 21-23 and 33 under 35 U.S.C. § 102(b) and claims 34, 9, 19, 29, and 32 under 35 U.S.C. § 103(a) as being unpatentable over JP 5-297412 in combination with Oh, Shimada, Someya, Huffman or Takano, respectively, are respectfully traversed and reconsideration is requested. Independent claims 1, 33 and 34 are allowable over the cited references in that each of these claims recites a combination of elements including, for example, "a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region, said pixel region being divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other." None of the cited references including JP 5-297411, Oh, Shimada, Someya, Huffman and Takano, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that independent claim 1 and claims 2-5, 9-19, 21-25, 27, 29 and 31, which depend from claim 1, independent claim 33, and independent claim 34 are allowable over the cited references.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

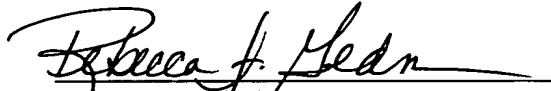
If the Examiner deems that a telephone call would further the prosecution of this application, the Examiner is invited to call the undersigned at (202) 624-1288. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. § 1.136, and any additional fees required under 37 C.F.R. § 1.136 for any necessary extension of time, or any other fees required to complete the

filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Respectfully submitted,

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A handwritten signature in dark ink, appearing to read "Rebecca A. Goldman", written over a horizontal line.

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MARKED UP VERSION OF CLAIMS TO SHOW CHANGES MADE

1. (Amended) A multi-domain liquid crystal display device comprising:

- first and second substrates facing each other;
- a liquid crystal layer between said first and second substrates;
- a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region, said pixel region being divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other;
- a pixel electrode electrically charged through said data bus line in said pixel region;
- a common-auxiliary electrode surrounding said pixel electrode on a same layer whereon said gate bus line is formed;
- a gate insulator over said whole first substrate;
- a passivation layer on said gate insulator over said whole first substrate;
- a light shielding layer on said second substrate;
- a color filter layer on said light shielding layer;
- a common electrode on said color filter layer; and
- an alignment layer on at least one substrate between said first and second substrates.

33. (Amended) A multi-domain liquid crystal display device comprising:

- first and second substrates facing each other;
- a liquid crystal layer between said first and second substrates;
- a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel

region, said pixel region being divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other;

a pixel electrode electrically charged through said data bus line in said pixel region; and

a common-auxiliary electrode surrounding said pixel electrode on a same layer whereon said gate bus line is formed.

34. (Amended) A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region, said pixel region being divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other;

an n-line thin film transistor at a crossing area of said gate and data bus lines;

a pixel electrode electrically charged through said data bus line in said pixel region; and

a common-auxiliary electrode surrounding said pixel electrode on a same layer whereon said gate bus line is formed;

a gate insulator over said whole first substrate;

a passivation layer on said gate insulator over said whole first substrate;

a light shielding layer on said second substrate;

a color filter layer on said light shielding layer;

a common electrode on said color filter layer; and

an alignment layer on at least one substrate between said first and second substrates.